

NPN Silicon Transistor

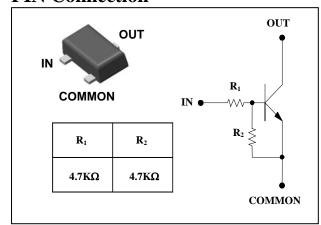
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
SRC1201UF	<u>R1</u> <u> </u>	SOT-323F

①Device Code ② Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	V _I	20,10	V
Output current	Io	100	mA
Power dissipation	P_D	200	mW
Junction temperature	T _J	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

 $(Ta=25^{\circ}C)$

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	$V_0 = 5V$, $I_0 = 10mA$	30	55	-	-
Output voltage	V _{O(ON)}	I _O =10mA, I _I =0.5mA	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V$, $I_0 = 5mA$	-	1.5	2.0	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = 5V$, $I_0 = 0.1 \text{mA}$	1.0	1.2	-	V
Transition frequency	f _T *	$V_O=10V$, $I_O=5mA$, $f=1MHz$	-	200	-	MHz
Input current	I_1	$V_1 = 5V, I_0 = 0$	-	-	1.8	mA
Input resistor (Input to base)	R ₁	-	3.3	4.7	6.1	KΩ
Input resistor (Base to common)	R_2	-	3.3	4.7	6.1	KΩ

^{* :} Characteristic of transistor only

KSD-R5D002-002

Electrical Characteristic Curves

Fig. 1 I_{O} - $V_{I(ON)}$

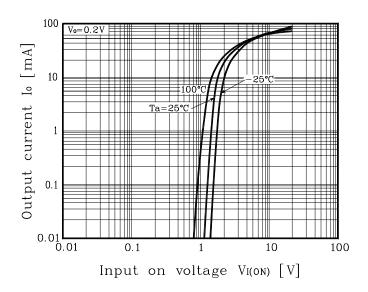


Fig. 2 I_{O} - $V_{I\left(OFF\right)}$

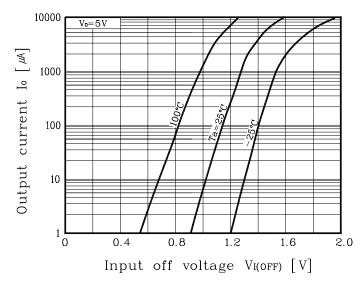
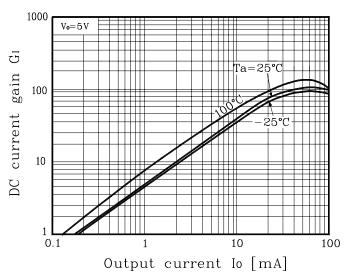
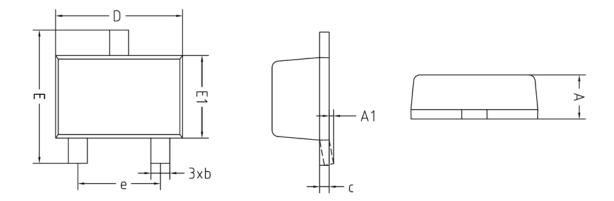


Fig. 3 G_I - I_O



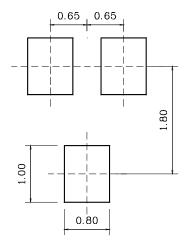
2

Outline Dimension



SYMBOL	1	NOTE		
STRIBUL	MINIMUM	NOMINAL	MAXIMUM	NUTE
Α	0.60	-	0.80	
A1	0.00	-	0.10	
Ь	0.30	-	0.40	
С	0.08	-	0.16	
D	1.90	2.00	2.10	
Е	1.95	2.10	2.25	
E1	1.20	1.30	1.40	
е	1.30BSC			

*Recommend PCB solder land [Unit: mm]



KSD-R5D002-002

3

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KSD-R5D002-002